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11. The cambered LCD panel of claim 1, wherein a gap between the first substrate and the second substrate ranges from about 2 to about 6 micrometers.

12. The cambered LCD panel of claim 1, wherein a gap between the first substrate and the second substrate ranges 5
from about 3 to about 5 micrometers.

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13. The cambered LCD panel of claim 1, wherein the first substrate is a color filter substrate, and the second substrate is a transistor array substrate.

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